



*Language* Version Date English V1.0 2010/1/26

## Included items

- One PMDK card ( and some optional cables and daughter board)
- One software installation CD (2010/1/26 or newer)
- One Quick Start Guide (this document)



1. Please execute **PMDKsetup.exe** which is locate in

## CD:\PMDK\setup\WinXP\PMDKsetup.exe



This windows driver only support Windows XP/NT version

2. Follow the instructions shown on the screen and complete the installation.

# **3** Hardware installation

## Following steps below to install card:

- 1. Turn off computer power
- 2. Open the cover of computer case
- 3. Choose an unused PCI slot
- 4. Plug the PMDK card into this slot carefully and use a screw to fix it
- 5. Install the cover back
- 6. Turn on the power supply

Start the computer and enter Windows operation system, the system will execute the **Plug&Play** driver. After the driver is installed, PMDK is ready for testing.

# Pin assignments

Please refer to the user manuals of PMDK and its daughter boards.



## > PMDK

- 1. Configuration:
- Daughter board (optional). For example, DN-8368M)
- DC 24V power supply. For example, DP-665(optional)
- Servo motors and drives (optional). For example, MITSUBISHI J2S series)
- 2. Connect the PMDK card and its daughter boards with cables.
- 3. Supply +24V power to daughters by connecting the E24V and EGND of power supply to the daughter boards.
- 4. Execute the Sample01.exe on PC. This program is locate at

C:\PMDK\Utilities\Sample01.exe

5. Referring to the Fig. 1, the Interrupt Count will keep

counting up. If the drives and motors are not wired to the PMDK card, users can test the read/write of DPRAM. CMD code = 0x2504 is used to clear LP (logical position) and EP (encoder position). Press Exec CMD button while CMD code = 0x2504 to test if the action is OK or not.

PLD Versi	ion: [1.8] Get Dip-	Switch	Interru DPRAM	upt Count : INT code :	223 0x3001		CMD code:	0x2504 Exec CMD	
	ffset (hex)	: 0×000		(0x000 /	~ 0x7FF)				
WORD Access				DWORD Access					
0x5A5A		Write Read		0×5A	Write				
					Read				
			E	ntry Acces	s (16 WORI	D)			
0×0000	0×0000	0×0000	0×0000	0×0000	0×0000	0×0000	0×0000	1	
0×0000	0×0000	0×0000	0×0000	0×0000	0×0000	0×0000	0x0000	Write	
								Read	
LP	0	0		0	, [0		0	0	
ED	-21490			0	[0			0	
CP	21490	10		Jo	10		lo.	Jo.	
	0	0		0	0		0	0	

Fig. 1

6. If the DN-8368 is wired to daughter board, change the DA value and measure the output at the daughter board to verify the DA function. The steps are as follows: a) set the DA2 to be 2; b) write 0x2505 to CMD code; c) press Exec CMD button. Use a voltage meter to check if the DA2 is outputting around 2.0 Volt.

PLD Vers	ion: [1.8] Get Dip	Switch	Intern DPRAM	upt Count : INT code :	424 0×3001			ode: 0x2505 Exec CMD
OPRAM C	ffset (hex)	: 0×000		(0x000	~ 0x7FF)			
	WORD	Access —				DWC	RD Access	
0x5A5A		Write		0x5A5A5A5A				Write
		R	ead					Read
			E	intry Acces	s (16 WORI	D)		
0×0000	0×0000	0×0000	0×0000	0×0000	0×0000	0×0000	0×0000	
0×0000	0×0000	0×0000	0×0000	0×0000	0×0000	0×0000	0×0000	Write
								Read
IP	0	0		0	0		0	0
LI	0	0		0	0		0	0
EP	1-							

Fig. 2



# **Reference Information**

## ✓ PMDK website:

http://www.icpdas.com/products/DAQ/pc\_based/pmdk.htm

### ✓ Optional Accessories website:

http://www.icpdas.com/products/DAQ/screw\_terminal/dn-8368.htm http://www.icpdas.com/products/DAQ/screw\_terminal/dn\_20m.htm

### ✓ Manuals:

CD:\PC\_Base\PCI\PMDK\Manual http://www.icpdas.com/products/motion/download%20data/Motion\_download\_ PMDK.htm

### ✓ Related products:

http://www.icpdas.com/products/motion/download%20data/Motion\_download\_ PMDK.htm

(Note: the Motion\_download\_PCbased has underscore characters.)

#### The main website of ICP DAS:

http://www.icpdas.com



- Professional technical support
- Agents and ordering information
- Enhancing device functions
- Q and A

contact method Service@icpdas.com

Application Examples

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